

EAST (7849.wsp:1)

File View Edit Tools Window Help

60 and (spacer near recess3)

US-PPH-OR USPAT

Default gateway: OR

60 and (spacer near recess3)

60 and (spacer near recess3)

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XR	Retrieval C	Inventor	S	C	P	3
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040819	40	Method of manufacturing a semiconductor integrated circuit	257/331			Lee, Sang-Eun et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040812	52	Methods of forming polymer films and of using such polymer films	438/758			Xu, Songlin et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040812	53	Methods of using polymer films to form micro-devices	438/694			Xu, Songlin et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040708	25	MOS TRANSISTOR	438/182			Doris, Bruce B. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040520	10	SIGE VERTICAL GATE CONTACT FOR GATE CONTACT	438/270			Hummeler, Klaus	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040506	18	Process for integration of a high dielectric constant gate in semiconductor devices	438/199	257/E21.633;		Wang, Ming-Fang et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040506	14	Semiconductor devices having contact plugs and local interconnects	257/296	257/E21.507;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040401	6	Method for producing a semiconductor component	438/585	257/E21.407;		Behammer, Dag	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20040311	13	Source and drain elements	117/84	257/E21.463;		Langdo, Thomas A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20030828	14	Semiconductor device having increased metal silicide partitioning	438/233	257/E21.634;		Wieczorek, Karsten et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20030529	23	Novel MOS transistor structure and method of fabrication	257/288	257/E21.636;		Murthy, Anand et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20030306	11	METHOD OF FORMING A SPACED	438/303	257/E21.435;		Kwon, Hyung-Shin et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
13	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20030116	15	METHOD OF FABRICATING NOVEL MOS TRANSISTOR		257/E21.435;		Deshpande, Sadanand et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
14	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20021219	17	Field emitter cell and array with vertical thin film edges	313/309	257/E21.641;		Hsu, David S.Y. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
15	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20021219	23	NOVEL MOS TRANSISTOR STRUCTURE AND METHOD OF FABRICATING	257/286	257/240		MURTHY, ANAND et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
16	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20021205	14	Semiconductor devices having contact plugs and local interconnects	438/618	257/E21.507;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
17	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20021205	14	SEMICONDUCTOR DEVICES HAVING CONTACT PLUGS AND LOCAL INTERCONNECTS	438/241	257/E21.507;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
18	<input type="checkbox"/>	<input type="checkbox"/>	US 20040160886	20020502	11	Method of fabricating cell of flash memory device	438/262	257/E21.682;		Lee, Seong-Soo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

☐ **Highly detailed and accurate**

 BGS form
 ES&R form
 Image
 Text
 PDF

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EAST (7849.wspal)

FileViewEditToolsWindowHelp

L63: (120) 60 and (spacer near2 recess

L64: (73) 60 and (spacer near recess

L65: (1) "6108988".PN.

L66: (1) "6071782".PN.

L67: (1) "5920783".PN.

L68: (1) "5882973".PN.

Draw

Draw

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Default gasistor OR

60 and (spacer near recess3)

Search

Highlight all hit items quickly

BPS form

TS&R form

Issue

Text

HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XR	Retrieval C	Inventor	S	C	P	3	
55	<input type="checkbox"/>	<input type="checkbox"/>	US 6159065 A	20001212	15	Method for manufacturing a	445/24	445/23		Jung, Sung Jae et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
56	<input type="checkbox"/>	<input type="checkbox"/>	US 6124177 A	20000926	9	Method for making deep	438/305	257/410;		Lin, Chrong Jung et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
57	<input type="checkbox"/>	<input type="checkbox"/>	US 6107141 A	20000822	11	Flash EEPROM	438/267	438/257		Hsu, Louis Lu-Chen et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
58	<input type="checkbox"/>	<input type="checkbox"/>	US 6084245 A	20000704	16	Field emitter cell and array	257/10	313/309;		Hsu, David S. Y. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
59	<input type="checkbox"/>	<input type="checkbox"/>	US 6074921 A	20000613	25	Self-aligned processing of	438/299	257/E21.31;		Lin, Xi-Wei	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
60	<input type="checkbox"/>	<input type="checkbox"/>	US 5953612 A	19990914	26	Self-aligned silicidation	438/299	257/E21.004;		Lin, Xi-Wei et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
61	<input type="checkbox"/>	<input type="checkbox"/>	US 5910912 A	19990608	12	Flash EEPROM with	365/185.01	257/321;		Hsu, Louis Lu-Chen et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
62	<input type="checkbox"/>	<input type="checkbox"/>	US 5904508 A	19990518	16	Semiconductor device and a	438/151	257/E21.413;		Codama, Mitsufumi et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
63	<input type="checkbox"/>	<input type="checkbox"/>	US 5736435 A	19980407	8	Process for fabricating a fully	438/151	257/E21.414;		Venkatesan, Suresh et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
64	<input type="checkbox"/>	<input type="checkbox"/>	US 5619097 A	19970408	76	Panel display with dielectric	313/495	313/309;		Jones, Gary W.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
65	<input type="checkbox"/>	<input type="checkbox"/>	US 5587623 A	19961224	75	Field emitter structure and	313/497	313/309;		Jones, Gary W.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
66	<input type="checkbox"/>	<input type="checkbox"/>	US 5548181 A	19960820	78	Field emission device	313/309	313/495;		Jones, Gary W.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
67	<input type="checkbox"/>	<input type="checkbox"/>	US 5529524 A	19960625	78	Method of forming a spacer	445/24	313/450		Jones, Gary W.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
68	<input type="checkbox"/>	<input type="checkbox"/>	US 5298444 A	19940329	8	Method for manufacturing a	438/571	257/213;		Ristow, Dietrich	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
69	<input type="checkbox"/>	<input type="checkbox"/>	US 5220186 A	19930615	12	Semiconductor device with a	257/284	257/280;		Kasai, Nobuyuki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
70	<input type="checkbox"/>	<input type="checkbox"/>	US 5089434 A	19920218	18	Mask surrogate	438/274	257/E21.033;		Hollinger, Theodore G.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
71	<input type="checkbox"/>	<input type="checkbox"/>	US 5045903 A	19910903	22	Topographic pattern	257/339	257/341;		Meyer, Theodore O. et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
72	<input type="checkbox"/>	<input type="checkbox"/>	US 4932693 A	19900612	10	Gate latch assembly and hinge	292/205	16/299;		Schwartz, Bruce H.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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